Utilization of Ready-Made Software in HPF Programs

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Abstract

Portable and efficient ways for calling numerical high performance software libraries from HPF programs are investigated. The methods suggested utilize HPF's EXTRINSIC mechanism and are independent of implementation details of HPF compilers. Two prototypical examples are used to illustrate these techniques. Highly optimized BLAS routines are utilized for local computations: (i) in parallel multiplication of matrices, and (ii) in parallel Cholesky factorization. Both implementations turn out to be very efficient and show significant improvements over standard HPF implementations.

1 Introduction

High Performance Fortran (HPF [7]) is one of the most interesting approaches for high-level parallel programming. In particular, it provides very convenient ways for specifying data distributions and for expressing data parallelism. Development of parallel code using HPF is much easier and requires less effort than message passing programming, for example, using MPI.

However, in numerical applications the performance achieved with HPF programs is often disappointing compared to message passing code. This is partly due to the immaturity of HPF compilers, which can be explained by the difficulties to implement advanced features of HPF efficiently. There is, however, another crucial aspect, which is often ignored. There is not enough support for integrating highly optimized library routines into HPF code.

Much effort has been spent on highly efficient implementations of the Basic Linear Algebra Subroutines (Blas [4]) and on numerical libraries for dense linear algebra, which use the Blas as building blocks. Important examples are Lapack [1] as the standard sequential library for dense (or banded) linear algebra methods and parallel libraries such as ScaLapack [2] or PLapack [6].

The integration of existing software into HPF is crucial for several reasons.

- In many scientific applications a lot of programming effort can be saved by using existing library routines as building blocks instead of (re-)coding them in HPF directly.

- A considerable amount of expertise has been incorporated into high quality software packages like the ones mentioned above. It is hardly possible to achieve comparable floating-point performance when coding in HPF directly. Even if a problem is well suited for an HPF implementation, the success of such an attempt heavily relies on the maturity of HPF compilers, which cannot be taken for granted at present (see Ehold, Gansterer, and Ueberhuber [5]).

- When standard numerical operations are coded in HPF the resulting code often suffers from poor local performance. Thus, one of the key issues is to optimize local performance in order to improve overall parallel performance.

- Highly optimized Blas routines are available for most target systems. Therefore, the use of Blas routines for local computations ensures *performance portability.*

- Usually the main motivation for parallelization is performance improvement. It is essential to optimize the *sequential* code first in order to be able to measure the benefits of parallelization in a clean way. The sequential optimization typically involves restructuring the code, for example, by increasing the fraction of Level 3 (matrix-matrix) operations and by using appropriate BLAS 3 routines (or other high performance library routines) wherever possible. Thus, when the parallelization is done by using HPF, the necessity of combining the BLAS (and other numerical packages and libraries) with HPF arises quite naturally.

For all these reasons the goal of this paper is to investigate ways to utilize high performance numerical libraries in an HPF context.

The basic concept provided by HPF for integrating procedures from other programming languages or from other programming models is the EXTRINSIC mechanism (HPF Forum [7]). Based on this concept, various different ways at different levels of abstraction for combining HPF with existing library routines will be outlined in this paper.

The main ideas and basic concepts presented in this paper are applicable to many numerical algorithms in dense linear algebra, but their implementation will differ in various technical details. For the purpose of illustration two operations, which arise very frequently at the core level of numerous scientific applications, serve as prototypes: Matrix-matrix multiplication and, representing more complicated numerical algorithms, Cholesky factorization. In Section 2 ways for integrating sequential library routines into HPF code are suggested for these two operations. Section 3 gives an overview of methods and attempts to integrate parallel library routines into HPF. In both cases experiments have been performed, which substantiate the considerable performance gains of numerical applications using the techniques suggested.

The remarkable benefit of the approaches described in this paper is that they are fully *portable*. In contrast to other methods they are independent of compiler or hardware specifics (Section 3). They only use language features of "core HPF" from the HPF standard and a few of the Approved Extensions (HPF Forum [7]) (the extrinsic kinds HPF_LOCAL and F77_SERIAL), which are supported by most HPF compilers.

2 Calling Sequential Routines from HPF

The topic of this section is the integration of extrinsic library routines into an HPF program for *local* computations only. All the communication in the multiprocessor environment is organized by the HPF compiler.

A matrix-matrix multiplication routine (Section 2.1) and a Cholesky factorization routine (Section 2.2) were implemented. Both utilize the BLAS for local computations. Experiments were performed with the HPF compiler from PGI, *pghpf*, version 2.4, on a Meiko CS-2. A standard Fortran implementation of the BLAS (LIBBLAS), compiled with the *pgf77* Fortran 77 compiler from PGI had to be used in the experiments. Further significant performance improvements are expected when highly optimized BLAS can be utilized.

2.1 Multiplication of Matrices

As a first example, the computation of the product $C \in \mathbb{R}^{m \times n}$ of two matrices $A \in \mathbb{R}^{m \times l}$, $B \in \mathbb{R}^{l \times n}$ is considered where all matrices can have arbitrary distributions. A new routine called `par_dgemm` (*parallel dgemm*) has been developed, which performs the matrix-matrix multiplication. Internally, this operation is split up into local operations on subblocks, each of which is performed by calling the general matrix multiplication routine `BLAS/*gemm`.

In the general case, the multiplication of two distributed matrices involves *non-local* computations. Some of the data have to be replicated over several processors in order to localize all the computations involved.

2.1.1 Loop Orders

The central operation in matrix-matrix multiplication is

$$C(i,j) = C(i,j) + A(i,k)B(k,j),$$

where $i = 1, 2, \ldots, m$, $j = 1, 2, \ldots, n$, and $k = 1, 2, \ldots, l$. Permuting the order of these three loops yields different algorithmic variants with different characteristics with respect to parallelization (and consequently, different requirements for expressing parallelism). The basic differences relevant for parallelization are mentioned here, depending on which index varies in the outermost loop.

k$*$ variant: The two inner loops perform the outer product of a column of A and a row of B. Since the entire matrix C is updated in place at every step of the outer loop, this variant exhibits no communication requirements for elements of C. Computation of a local block of C requires the corresponding parts of a column of A and of a row of B. In order to localize all the computations one column of A has to be replicated in the direction of the rows of C and one row of B has to be replicated in the direction of the columns of C.

i$*$ variant: The two inner loops compute a row of C by multiplying a row of A with B. In this case it is usually most efficient to parallelize over the matrix B. This, however, requires distributed reduction operations for the elements of C.

j$*$ variant: The two inner loops compute a column of C by multiplying A with a column of B. In this case it is usually most efficient to parallelize over the matrix A. This again requires distributed reduction operations for the elements of C.

The $k*$ variant requires the smallest amount of local storage without extra communication for the elements of C. This variant is well suited for two-dimensional data distributions, whereas for the other two variants either a one-dimensional data distribution is to be preferred (columnwise for the $i*$ variant, rowwise for the $j*$ variant) or extra communication needs to be performed for the elements of C.

In the context of linear algebra algorithms normally two-dimensional data distributions are used (for load-balancing reasons). In such a setup, the $k*$ variant exhibits the best data locality, minimizes the amount of data to be replicated, and leads to the most efficient parallelization. Consequently, it was chosen for the implementation.

2.1.2 Implementation

For performance reasons, a *blocked* version of matrix-matrix multiplication (Ueberhuber [9]) was implemented. Two levels of wrapper routines are involved. The programmer calls the routine `par_dgemm`, which takes the matrices A, B as input and returns the product matrix C.

In this routine the HPF_LOCAL routine `local_dgemm` is called inside a loop. In addition to the blocks involved `local_dgemm` also takes their size (the block size of the algorithm) as an argument and performs the local outer products of a block column of A and a block row of B by calling the routine `BLAS/dgemm`.

In `par_dgemm`, work arrays for the block column of A and for the block row of B are aligned properly with C, and then the corresponding subarrays of A and B are copied there. This copying operation adjusts the distribution of the currently required parts of A and B to that of C. It may cause a significant amount of communication, but it has the big benefit of making the procedure fully independent of the prior distributions of A and B. All the necessary communication, which is entirely contained in the copying operation, is restricted to the outermost loop.

2.1.3 Experiments

Fig. 1 illustrates the floating-point performance of `par_dgemm` and the intrinsic (parallel) function MATMUL for multiplying two $n \times n$ matrices on $p = 16$ processors. A and B were distributed cyclically in both dimensions, and C was distributed block-cyclically with block size 20 in both dimensions.

A considerable performance improvement of `par_dgemm` over MATMUL is evident (roughly by a factor of 2). This holds for other distribution scenarios as well. Moreover, for large matrix orders n the efficiency of `par_dgemm` improves, whereas the efficiency of the vendor supplied MATMUL routine decreases. Despite its speedup effect, the efficiency of `par_dgemm` is still at a disappointingly low level. Unfortunately it was not possible to use the highly optimized version of the BLAS as part of the SUN performance library (SUNPERF) because the HPF compiler currently available to the authors on the test machine does not support it. If a highly optimized BLAS library can be utilized instead of the standard version a significant performance improvement is to be expected.

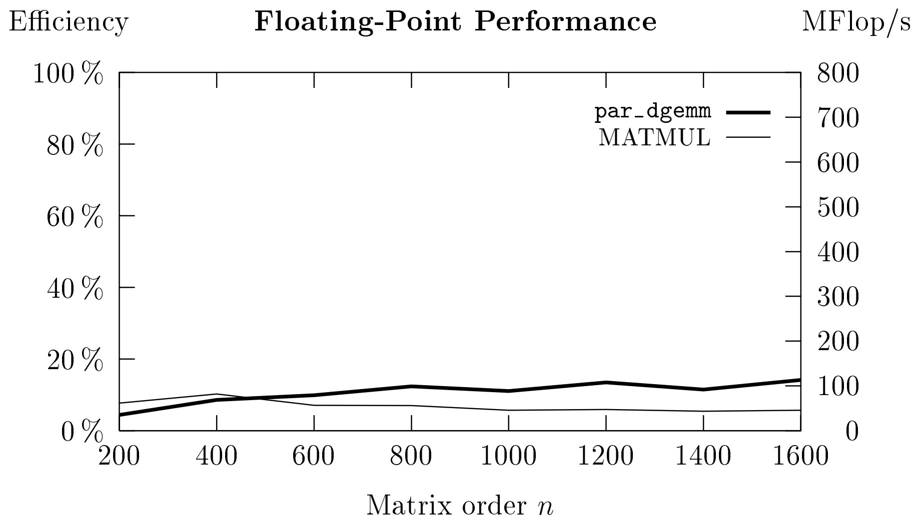

Figure 1: Matrix multiplication on 16 processors of a Meiko CS-2.

In order to evaluate parallel speedup (see Fig. 2), the runtime of the *best* available sequential version (which is a single call to `BLAS/dgemm`) was used as a reference value. Parallel speedup for `par_dgemm` is much better than for parallel MATMUL. The routine shows a satisfactory scaling behavior. The decreasing speedup for $p = 16$ processors indicates that the problem size $n = 1000$ becomes too small for an efficient parallelization.

2.2 Cholesky Factorization

Even if the HPF compiler is doing a good job with organizing data distribution and communication between processors, the performance achieved can be disappointing due to bad node performance. Comparisons with the efficiency of a highly optimized library based on the BLAS, show that the sequential BLAS ver-

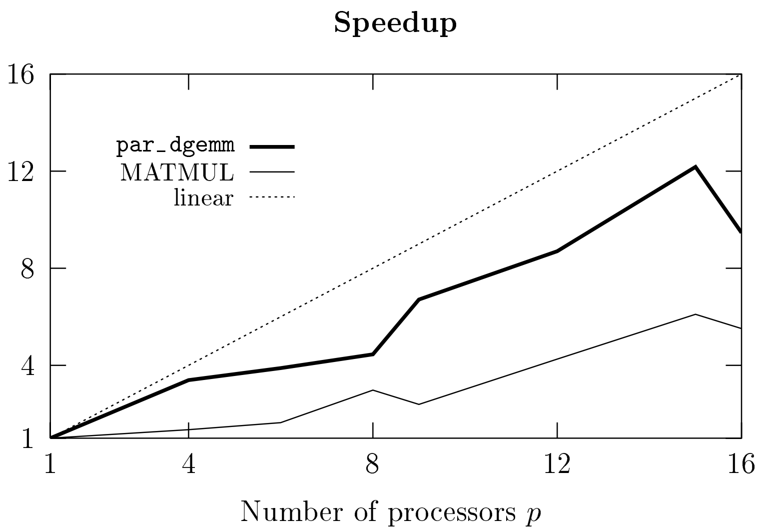

Figure 2: Matrix multiplication on p processors for $n = 1000$.

sion is up to 12 times faster than the fastest parallel HPF version on one processor and still 1.3 times faster than HPF on 16 processors (see Table 1).

Table 1: Cholesky factorization of a matrix of order 1000, using the vendor optimized `LAPACK/dpotrf` or pure HPF on a Meiko CS-2.

p	1	2	4	8	16
BLAS	**9 s**				
HPF	113 s	57 s	29 s	18 s	12 s

One method to improve the nodal performance in HPF is to use the BLAS for doing the local computation on each node. The blocked algorithm for computing the factor L of the Cholesky factorization $A = LL^\top$ of a real symmetric positive definite matrix, exhibits better locality of reference and is therefore to be preferred over the unblocked version on modern computer architectures (Anderson et al. [1], Ueberhuber [9]). A very important operation to be performed is the multiplication of certain submatrices of A.

The principle step of the blocked version of the Cholesky factorization is the following (see also Fig. 3).

$$\begin{pmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{pmatrix} = \begin{pmatrix} L_{11} & 0 \\ L_{21} & L_{22} \end{pmatrix} \begin{pmatrix} L_{11}^\top & L_{21}^\top \\ 0 & L_{22}^\top \end{pmatrix} = \begin{pmatrix} L_{11}L_{11}^\top & L_{11}L_{21}^\top \\ L_{21}L_{11}^\top & L_{21}L_{21}^\top + L_{22}L_{22}^\top \end{pmatrix}$$